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APPLICANTS

Yuji Yagi, Osaka-shi, JAPAN;
 Takeo Yasuho, Neyagawa-shi, JAPAN;

** CONTINUING DATA *****

THIS APPLICATION IS A DIV OF 09/332,968 06/15/1999 US PATENT NO. 6,218,736

** FOREIGN APPLICATIONS *****

JAPAN 10-166648 06/15/1998
 JAPAN 10-355837 12/15/1998

IF REQUIRED, FOREIGN FILING LICENSE
GRANTED ** 02/09/2001

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 11 13	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>			

ADDRESS

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*Title: a circuit board having simultaneously and
 simultaneously and
 circuit board and semiconductor device, and method of manufacturing the same*

FILING FEE RECEIVED 710	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other <input type="checkbox"/> Credit
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